

PS/Election Pholos

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

T. MIYAMOTO et al

Serial No. 10/046,446

Group Art Unit: 2811

Filed: January 16, 2002

Examiner: D. Owens

For:

SEMICONDUCTOR DEVICE AND MANUFACTURING

METHOD OF THAT

## RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents Alexandria, VA 22313

Sir:

In response to the Restriction Requirement dated

July 14, 2003, Applicants elect Group I, claims 1-24, drawn to

a semiconductor device, without traverse.

The Commissioner is hereby authorized to charge any fee that may be required to Deposit Account No. 50-1417.

Respectfully submitted,

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Date: August 13, 2003

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